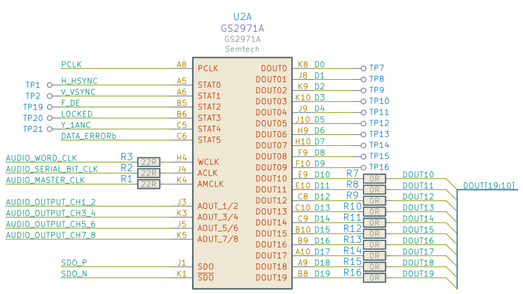
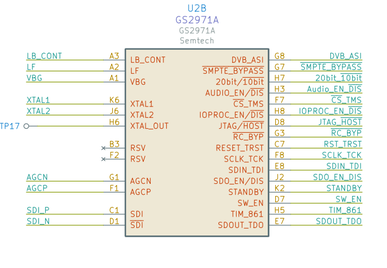
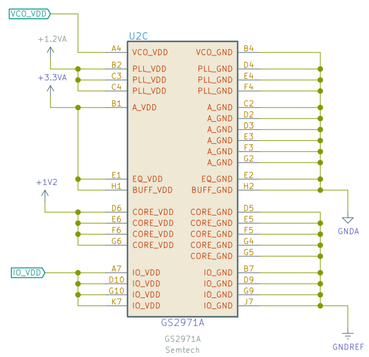
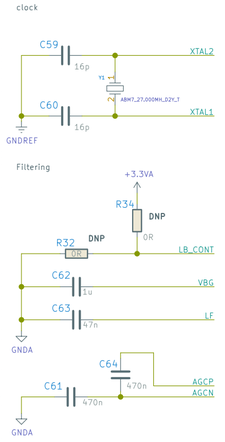
SDI deserializer



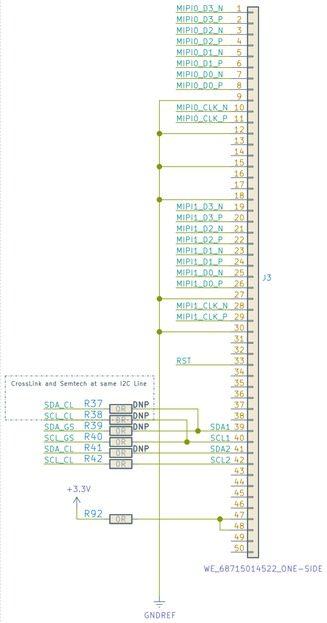




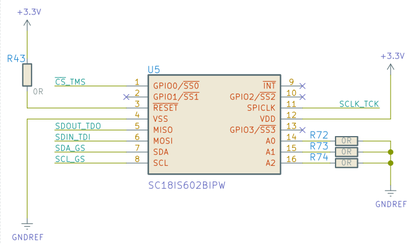
Place close to GS2971A



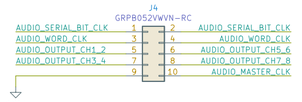
MIPI connector



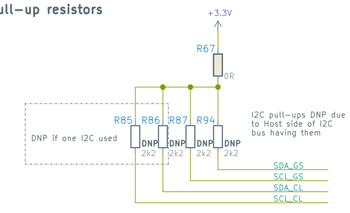
I2C to SPI bridge



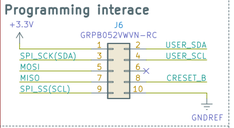
I2S connector



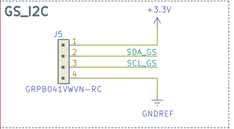
Pull-up resistors



Programming interace



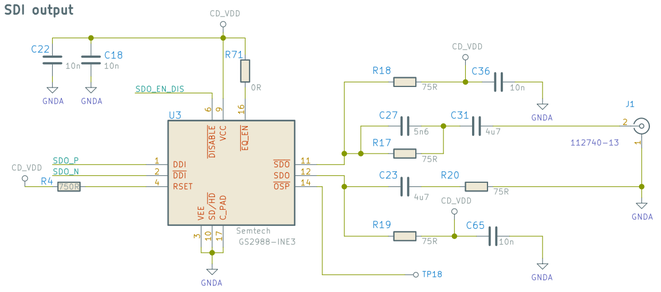
GS_I2C



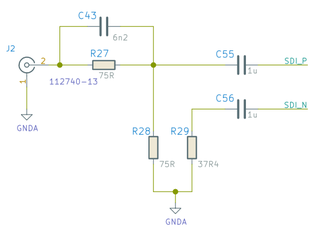
Logotypes

Logo N1 antmicro_logo Logo N2 oshw_logo

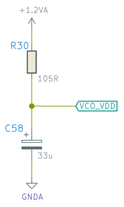
SDI output



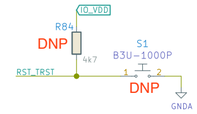
SDI input



Filtering



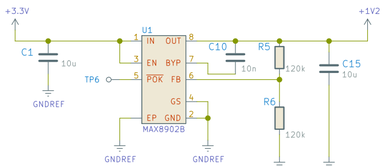
RESET



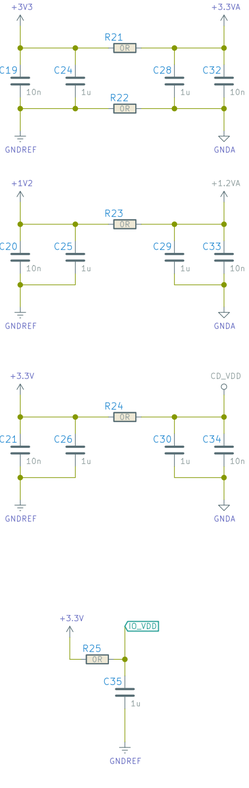
DNP S1 and replace with 1206 10K resistor.
This improves reliability of bitstream loading as U2 is kept in reset.

Power supply

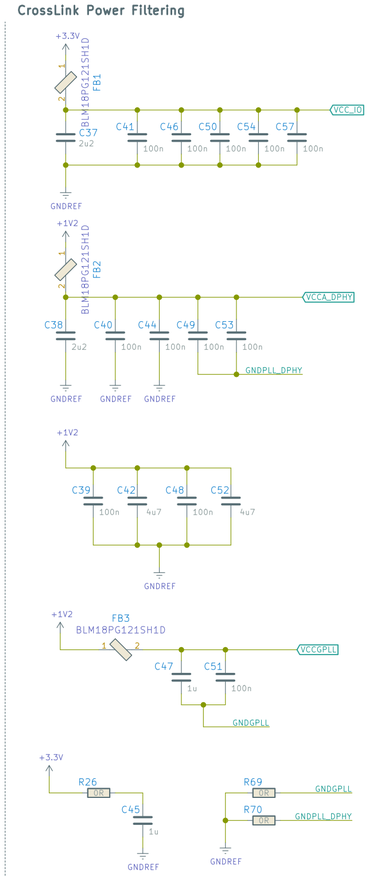
Low noise LDO



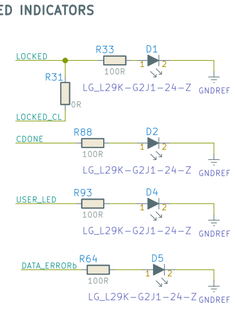
GS Power Filtering



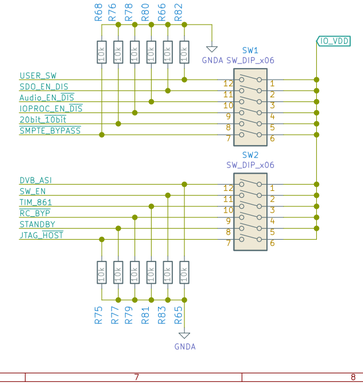
CrossLink Power Filtering



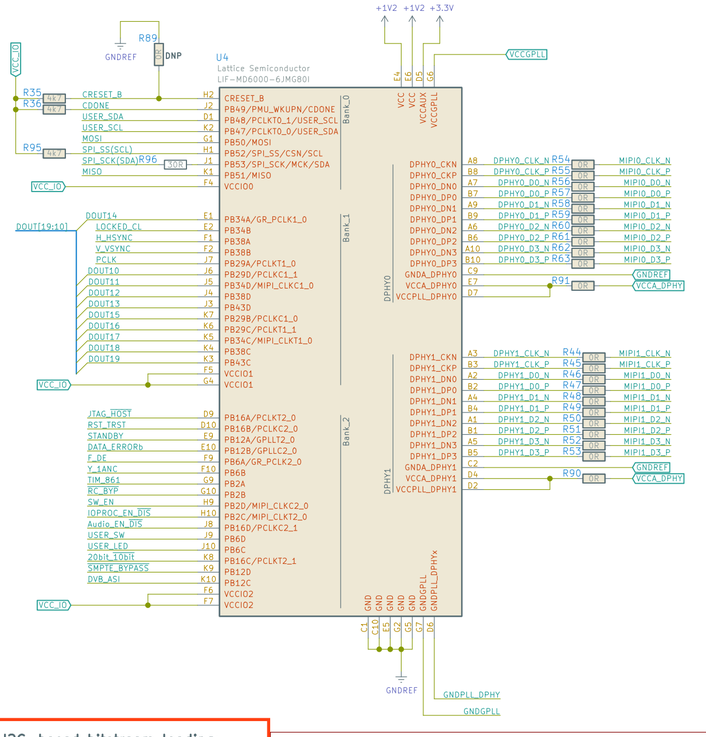
LED INDICATORS



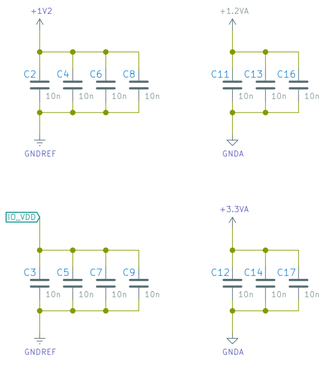
Configuration DIP Switches



CrossLink MIPI transmitter



Power decoupling



Rework for I2C-based bitstream loading:
U5 GPIO2 [pin 10] connected to CRESET_B net
SPI_SCK(SDA) net connected to SDA_GS
SPI_SS(SCL) net connected to SCL_GS

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Sheet: /
File: sdi-mipi-bridge.sch

Title: SDI-MIPI Bridge

Size: A2 | Date: | **Rev: 1.2.0**
KiCad E.D.A. kicad (5.1.8-0-10_14) | Id: 1/1